

# PCB Layout Quote Preparation Checklist

Project Information to Prepare Before Requesting a PCB Layout Quotation  
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|                       |  |               |  |
|-----------------------|--|---------------|--|
| Project / Board Name: |  | PCB Revision: |  |
| Target Schedule:      |  | Prepared By:  |  |
| Review Date:          |  |               |  |

| Category                         | Information to Prepare                                                                                                                                                                                               | Status / Notes                      |
|----------------------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-------------------------------------|
| Schematic                        | Provide the latest available schematic or identify which sections are still under development.                                                                                                                       | ■ Available ■ To Be Confirmed ■ N/A |
| BOM / Component Information      | Provide available component information, including major ICs, BGA / FPGA packages, connectors and special components.                                                                                                | ■ Available ■ To Be Confirmed ■ N/A |
| Board Outline and Dimensions     | Provide the available board outline, dimensions and mechanical interface requirements.                                                                                                                               | ■ Available ■ To Be Confirmed ■ N/A |
| Mechanical Constraints           | Identify connector locations, mounting holes, keepouts, height restrictions, enclosure constraints and other fixed mechanical requirements.                                                                          | ■ Available ■ To Be Confirmed ■ N/A |
| Target Layer Count / Stackup     | Provide an existing stackup if available, or indicate whether stackup development / coordination is required.                                                                                                        | ■ Available ■ To Be Confirmed ■ N/A |
| BGA / FPGA Information           | Identify major packages, available pitch / package information and any known escape or interface constraints.                                                                                                        | ■ Available ■ To Be Confirmed ■ N/A |
| High-Speed Interfaces            | List applicable interfaces such as DDR, PCIe, USB, Ethernet, clocks or other constrained signals.                                                                                                                    | ■ Available ■ To Be Confirmed ■ N/A |
| Controlled Impedance             | Identify controlled-impedance requirements, target values and relevant nets / interfaces where already defined.                                                                                                      | ■ Available ■ To Be Confirmed ■ N/A |
| HDI / Via Requirements           | Identify known blind, buried, microvia, via-in-pad or other special via requirements, or note if via strategy still needs engineering review.                                                                        | ■ Available ■ To Be Confirmed ■ N/A |
| RF / Analog / Power              | Identify sensitive RF, analog, power, thermal or grounding requirements that may affect layout planning.                                                                                                             | ■ Available ■ To Be Confirmed ■ N/A |
| SI / PI Scope                    | Indicate whether SI / PI constraint development, simulation or formal post-layout review is required in addition to PCB layout.                                                                                      | ■ Available ■ To Be Confirmed ■ N/A |
| Required Deliverables            | Select desired outputs: ■ Editable PCB source files ■ Gerber / ODB++ ■ NC Drill ■ Fabrication drawing ■ Pick-and-place ■ Assembly drawing ■ Stackup documentation ■ Design / constraint documentation ■ Other: _____ | ■ Available ■ To Be Confirmed ■ N/A |
| Review and Revision Expectations | Identify the expected customer review process, likely design-change stages and post-layout support requirements.                                                                                                     | ■ Available ■ To Be Confirmed ■ N/A |
| Target Schedule                  | Provide the target project milestone or delivery expectation and identify any known schedule constraints.                                                                                                            | ■ Available ■ To Be Confirmed ■ N/A |

**Important:** PCB layout quotations should be based on the actual engineering scope and project constraints. Pin count may help estimate project size, but it does not by itself determine final PCB layout pricing.

Request a PCB Layout Quote: [www.excelcircuit.com/request-a-quote.html?service=design](http://www.excelcircuit.com/request-a-quote.html?service=design) | PCB Design & Layout Services: [View service page](#)